

Docket No.: 33227/189003; P1391-US-DIV-1
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Marc Tremblay

Confirmation No.: 7166

Application No.: 09/583,097

Art Unit: 2183

Filed: August 2, 1999

Examiner: D. J. Huisman

For: GROUPING LOGIC CIRCUIT IN A
PIPELINED SUPERSCALAR PROCESSOR

TRANSMITTAL LETTER

MS Issue Fee
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Enclosed are the following items for filing in connection with the above-referenced Patent Application:

1. Part B – Fee(s) Transmittal (1 page);

The Director is hereby authorized to charge any deficiency in the fees filed, asserted to be filed or which should have been filed herewith (or with any paper hereafter filed in

Application No.: 09/583,097

Docket No.: 33227/189003; P1391-US-DIV-1

this application by this firm) to our Deposit Account No. 50-0591, under Order No. 33227/189003; P1391-US-DIV-1.

Dated: August 20, 2008

Respectfully submitted,

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